



Pôle européen de compétence  
en simulation numérique haute performance  
European technopole dedicated to high-performance  
simulation and computing

# Forum TERATEC 2011

28 & 29 juin / June, 28 & 29  
École Polytechnique Palaiseau - France

## Calcul intensif et Conception de systèmes HPC and Systems Design

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# The Teratec development

*Gérard Roucairol,  
Chairman of Teratec*



# Activity growth

## ☺ A larger Scope

- ☺ From Classical Supercomputing to System Design, «Big Data » Processing, Multimedia Creation....

## ☺ Program extensions

- ☺ From collaborative R&D projects to European and international cooperations, New HPC master (MHIPS)

## ☺ Membership growth

- ☺ Both industrial leaders and technology start-ups

## ☺ Forum Teratec: Increased participation

- ☺ Exhibitors : +30% - Participants : +20%



# The Teratec Technopole is now a reality



**October 25, 2010 - Official opening of the CEA Very Large Computing Center**

*By Valérie Pécresse, Minister for Higher Education and Research and Bernard Bigot, CEA General Administrator*



**May 5, 2011 - Laying the first stone of the TERATEC CAMPUS**

*With more than 150 participants, local authorities, partners and members*



# The Teratec Technopole

Major HPC  
Industrial firms  
(large and small)

CEA Very Large  
Computing Center

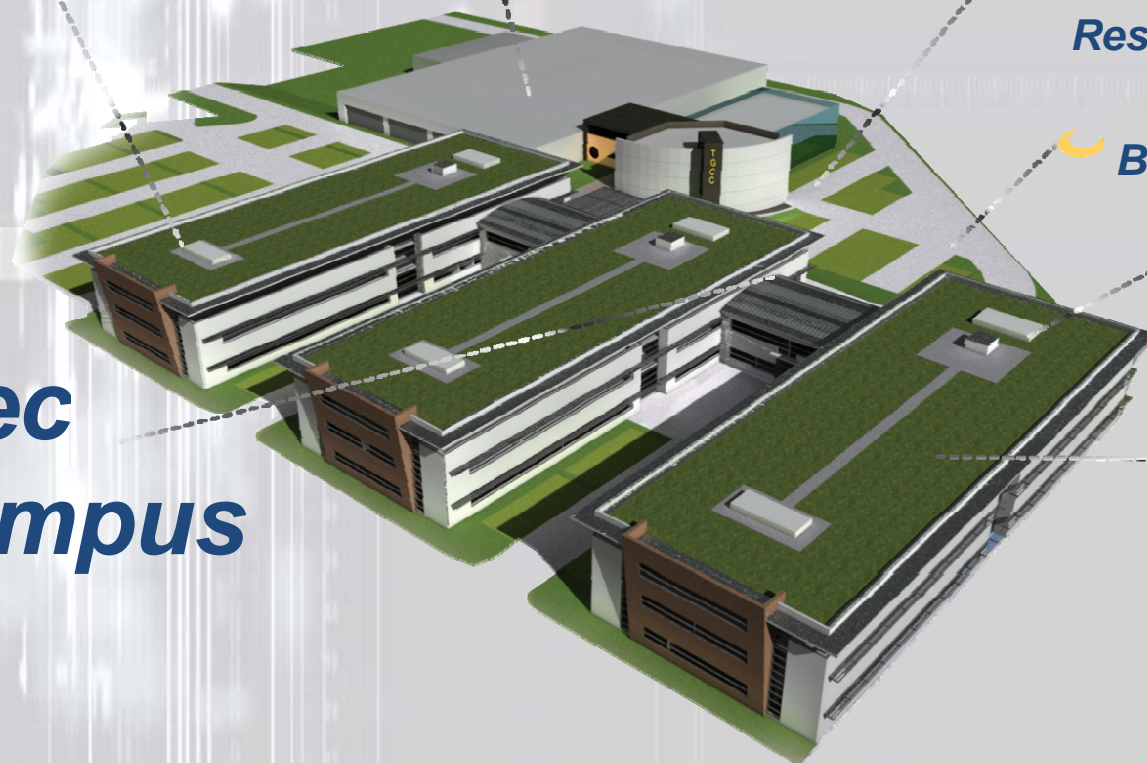
Collaborative  
Industry  
Research laboratories

Business Incubator

European HPC  
Training  
Institute

Service  
Platforms

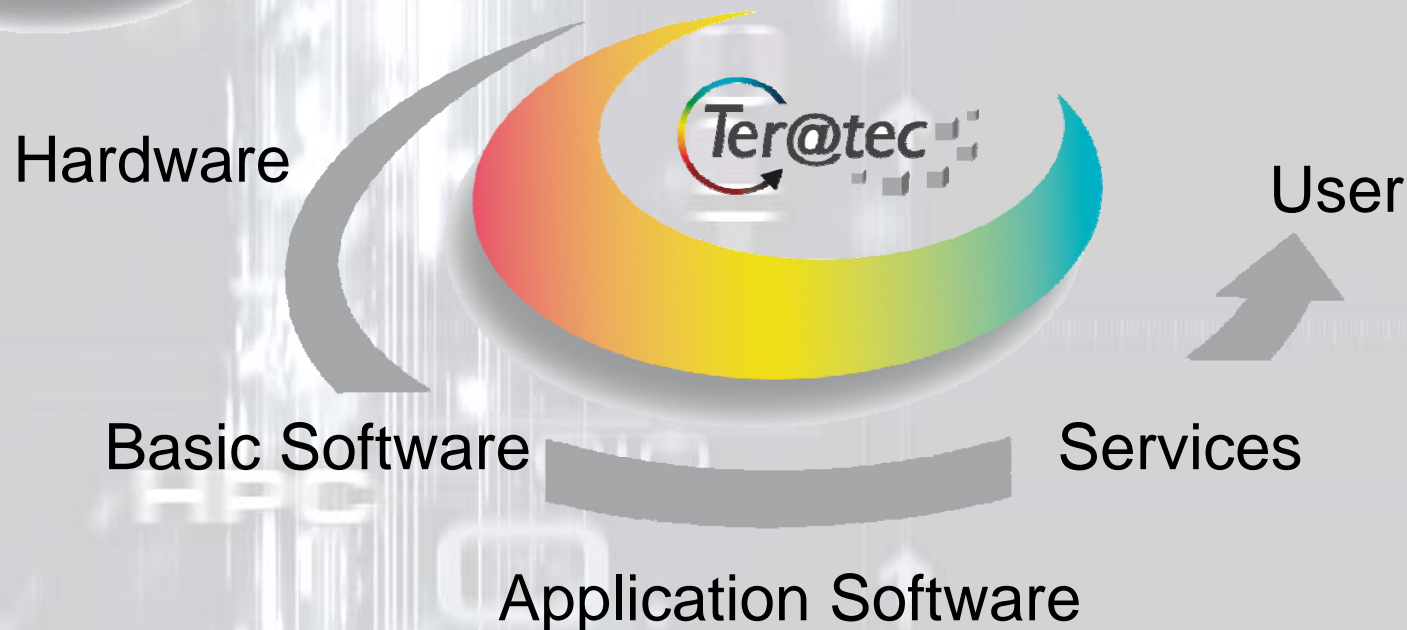
**Teratec  
Campus**







# A unique WW position



**Facing dramatic challenges and ruptures  
by covering at the **SAME** place  
**EVERY** step of the value chain**



Thank you for your attention  
&  
Welcome to the TERATEC Forum



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