

Intel Technical
Computing



Teratec '12

Stephan Gillich

Director

HPC & Workstation

EMEA

Intel® GmbH



Intel France



2012

Meudon: HQ, Sales & Marketing

Saclay: R&D, HPC and Data Centers (ILE)

Toulouse: R&D, Ultra Mobility

Sophia Antipolis: R&D, Certification, Mobility

Sophia Antipolis: iMC Mobile Communications

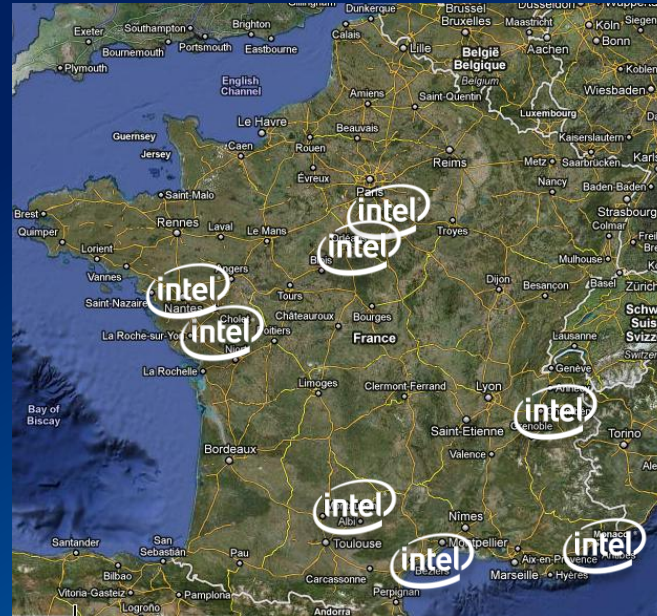
Montpellier: R&D SW, Open Source Tec Center

Nantes: ic modelisation

2009

Meudon: HQ, Sales & Marketing

Sophia Antipolis: Certification, Mobility



Windriver, McAfee



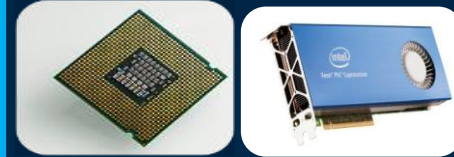
Intel in High Performance Computing



**Terascale
Research**



**Manufacturing
Process**



**Leading
Performance,
Performance/
Watt**



**Platform
Building
Blocks**



**Defined
HPC
Application
Platform**



**Broad SW
Tools
Portfolio**



**Interconnect
Technology**



**Dedicated,
Renowned
Expertise**



**Large Scale
Clusters
for Test and
Optimization**

Strategic Commitment to High Performance Computing

Other brands and names are the property of their respective owners.



Announcing Breakthrough Performance on Intel® Xeon® Phi™ Coprocessor (Codenamed Knights Corner)

>1

TFLOPs

Linpack (HPL) in a node



118

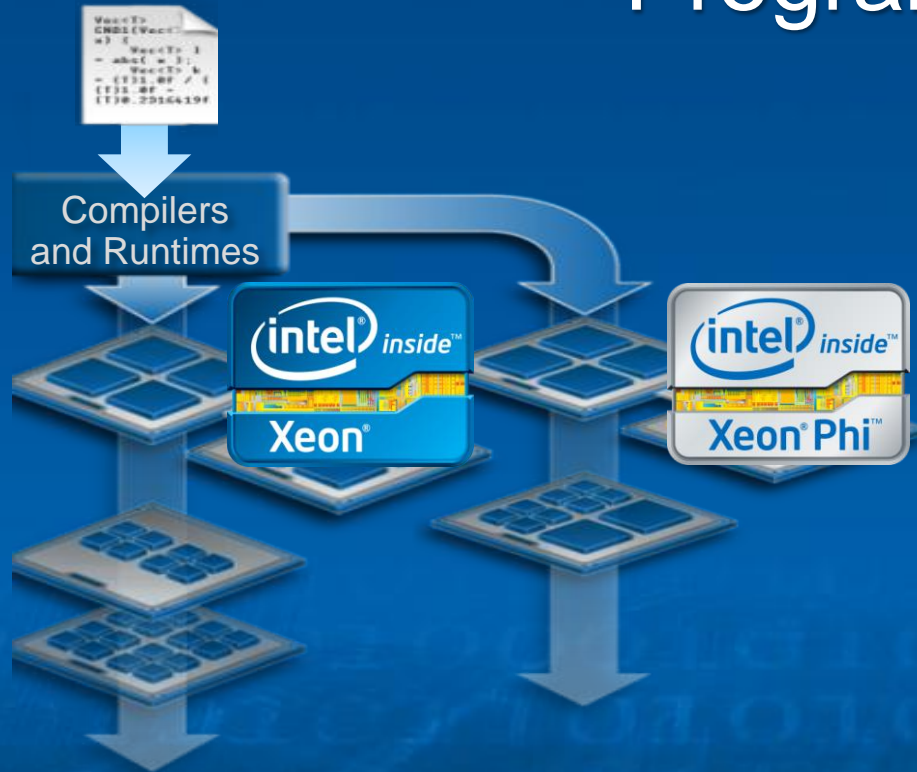
TFLOPs

Entry into the Top500

In Production In 2012, Enabled by 22nm 3-D TriGate Transistors



Game Changer for HPC Performance & Programmability



“Unparalleled productivity... most of this software does not run on a GPU”

Dr. Robert Harrison, NICS, ORNL