Forum Teratec 2018 19 & 20 JUIN JUNE 19 & 20

THE INTERNATIONAL MEETING FOR HPC, SIMULATION, BIG DATA

Tuesday 19th & Wednesday 20th of June, 2018
Ecole Polytechnique – Palaiseau, France
– 13th edition –

Teratec Forum 2018:
All about our program!

TERATEC FORUM, the meeting of international experts in High Performance Computing (HPC), Digital Simulation and Big Data will be held for its 13th edition on June 19 and 20 in Palaiseau, on the Ecole Polytechnique campus.

■ **Covering the hardware, software, services and R&D sections of the Teratec Forum**, walk around an **exhibition of nearly 70 stands, and thematic areas** such as the European Research Café.

■ The **Conferences section** will help you taking stock of the most recent developments in digital technologies, along the **plenary sessions scheduled on Tuesday 19 June**:

Major industrial users (**Renault**, **Naval Group**) will attest to the challenges of high-performance simulation met in their companies with analysis of strategies to be implemented to master this.

Technology providers (**ARM**, **Mentor Graphics**) and innovative SMEs (**ActiveEon**) will present their latest achievements and show how these technologies are now accessible to SMEs.

Public authorities as well will to point out their role to develop these technologies profoundly transforming the worlds of research and education (**EPFL**, **ENS Paris-Saclay**, **Ecole Polytechnique**,...) like industry, thus allowing to expand with all breakthrough products and services we need to remain at the forefront of competitiveness.

■ **Eight technical and application workshops** led by experts, **on Wednesday 20 June**, will allow us to review emerging technologies and new application sectors in HPC, digital simulation and Big Data.

Predictive maintenance and digital twins. This workshop, chaired by Marie-Christine SAWLEY (Intel) and Jacques DUYSENS (Ansys), will focus on the digital twin that uses information from sensors on the actual installation to enable our understanding of system operation and foresee maintenance operations required to ensure optimum performance.

Health and personalized medicine in the era of HPC. Workshop, chaired by Emmanuel DEQUIER and Pierre TAMBOURIN (Genopole): describing how HPC facilitates the sequencing of the human genome, which will revolutionize both therapeutic practices becoming more precise and new drugs development adapted for each patient.

The quantum revolution on the way. This workshop, chaired by Guillaume COLIN DE VERDIERE (CEA), will explain how "quantum computing" will move from theoretical status to a phase of experimentation with development of a robust ecosystem, introducing usable simulators and new hardware solutions.

Cultural and creative industries at the cutting edge with digital. This workshop, chaired by Stéphane SINGIER (Cap Digital), will show how French digital creators have earned a worldwide reputation in the fields of modelling and animation, through testimonials from design studios and technology suppliers.

HPC, Cloud and Embedded Systems (HPC in the Loop). This workshop, chaired by Marie-Christine SAWLEY (Intel) and Jacques DUYSENS (Ansys), will explain how the most innovative systems will embark more and more computing power and use"HPC in the Loop" from both hardware and software sides.

The building and the infrastructures revisited by digital. This workshop, chaired by François PELEGRIN (Architect DPLG), will show how in less than a decade the world of construction has moved from 2D plans to BIM's advanced digital technologies to create a more efficient, sustainable and energy efficient urban environment.

Deep Learning through calculation practice. Chaired by Marc DURANTON (CEA), Laurent VANEL and Benoit VAUTRIN (IBM), this workshop will provide users of Artificial Intelligence and Deep Learning with insights into the advantages and applicability of these tools within their field, and enable them to base one practical opinion.

Autonomous systems and HPC/HPDA. This workshop, chaired by Luc MARBACH (Vedecom), why autonomous systems and vehicles, whatever their destination, will require the development of innovative numerical simulation tools to define, build, evaluate and validate recommended technical solutions.

■ **To conclude on a distinctive note, make sure to attend the 4th edition of the Digital Simulation Trophies** organized with **L'Usine Digitale**, in partnership with **CEA**, **Cray Computer**, **Dell EMC**, **HPE** and **Inria**. At this occasion, a jury of leading professionals will award 5 trophies **rewarding the Champions** of digital simulation, HPC and Big Data.

Book your Press badge now for TERATEC FORUM 2018 here

TUESDAY 19th AND WEDNESDAY 20th of JUNE, 2018 - ECOLE POLYTECHNIQUE, PALAISEAU (91120)

Check all information in real time on: Teratec Forum

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