

# BMW GROUPGLIMPSE INTO THE FUTURE WITH VISION VEHICLES. THE NEXT 100 YEARS

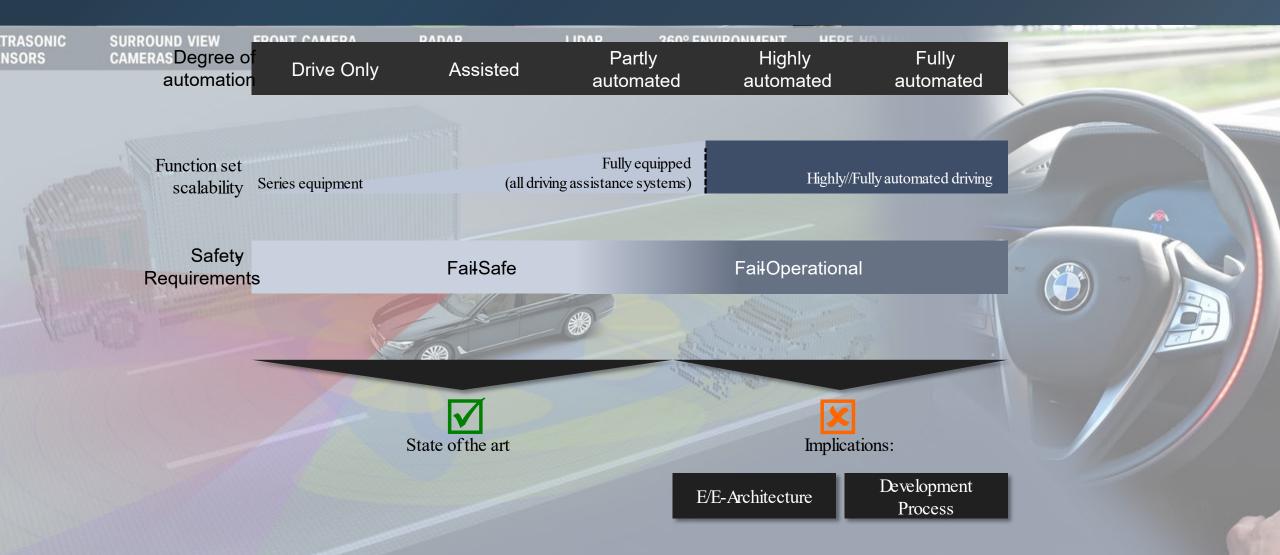
## ACES-A CORE ELEMENT OF BMW'S STRATEGY NUMBER ONE > NEXT.



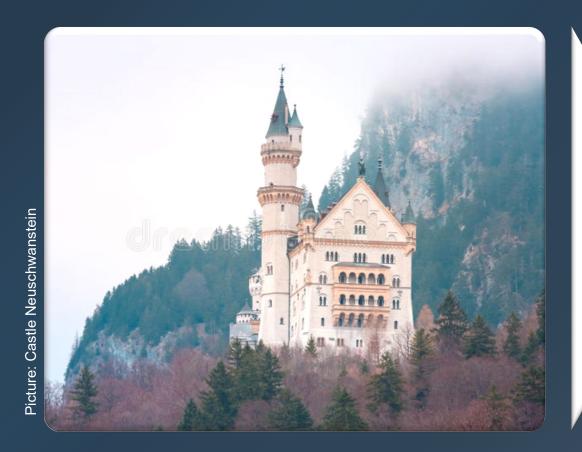
## THE LATEST BMW 5 SERIES. COMFORT AND SAFETY AT THE HIGHEST LEVEL.



## AUTOMATED DRIVING GENERATES NEW CHALLENGES FOR THE E/E ARCHITECTURE AND THE DEVELOPMENT PROCESS.



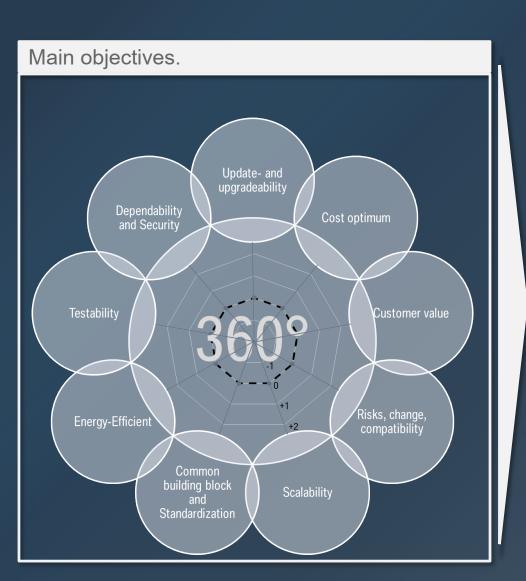
# THE FUTURE E/E ARCHITECTURE INCLUDING ITS DEVELOPMENT PROCE REQUIRES SUSTAINABLE CHANGES.





- Technical changes: Using more and more IT standards and establishing a homogenous system structure.
- Collaboration: Systemic thinking without borders using agile working methods.

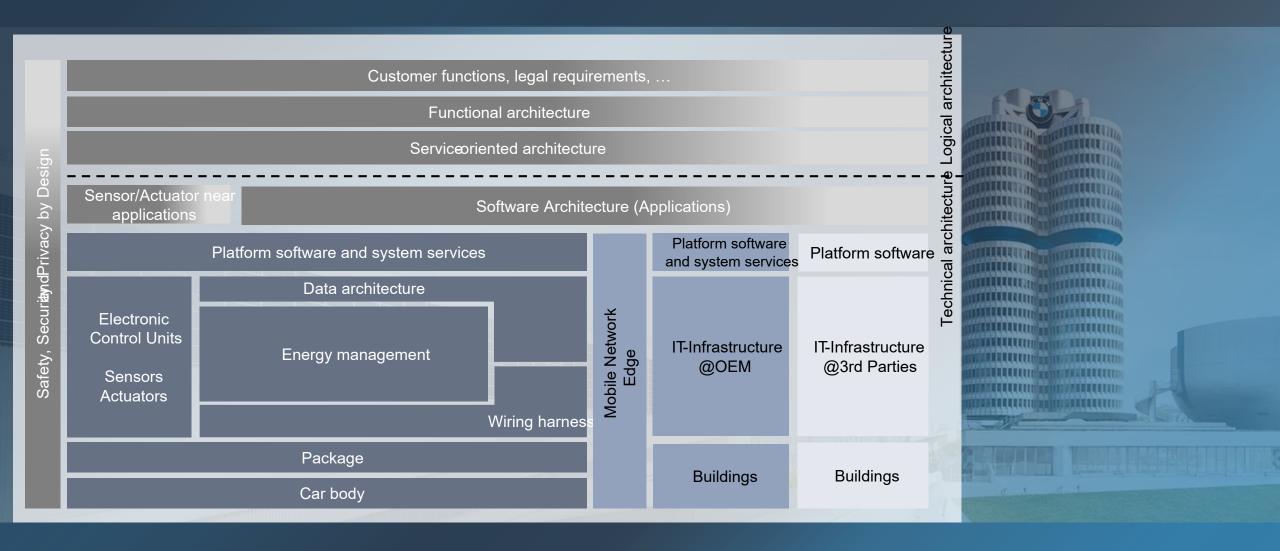
## THE NEXT GENERATION AND VISION OF THE E/E ARCHITECTURE ARCHITECTURE ALCOVER THE MAIN OBJECTIVES.



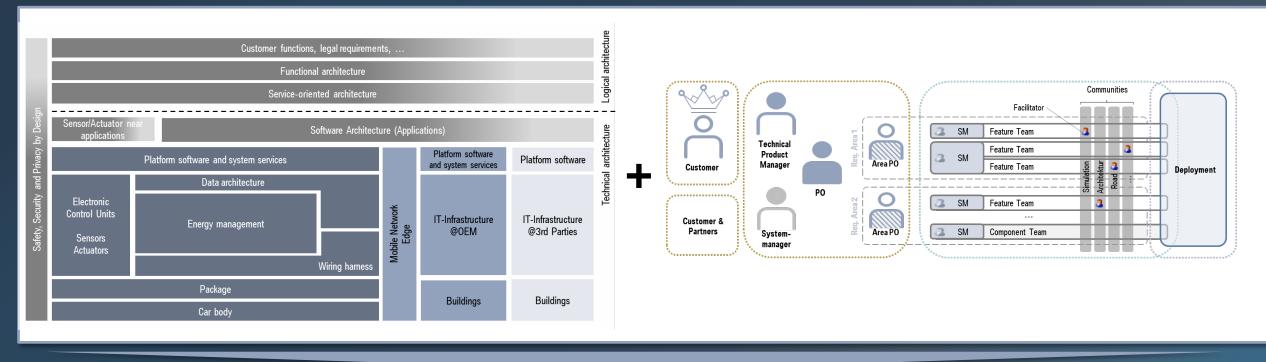
The e/e architecture strategy – Three step approach.

- The corporate strategy, legal requirements and game changers are important inputs for the e/e architecture vision.
- 2 Starting point is set by lessons learned based on the actual e/e architecture
- 3 The step size for the next generation of e/e architecture has been decided by the actual generation and the future vision.

## DERIVATION OF NECESSARY BUILDING BLOCKS FOR A SUSTAINABLE DIGITAL AUTOMOTIVE INFRASTRUCTURE.



# DERIVATION OF NECESSARY BUILDING BLOCKS FOR A SUSTAINABLE DIGITAL AUTOMOTIVE INFRASTRUCTURE.



## Service-oriented and und data architecture

Explicit and seamless APIs Semantic descriptions Change from Signtal IP-based communication

#### **Computing Platforms**

Update/Upgradeability
HW/SW codesign
High performance processors
Energy affective Computing

#### **Network architecture**

Zone orientation
Modular wiring harness
Intelligent fuse boxes
Optimized energy distribution

## Vehicle-backend infrastructure

Digital Twin
Connectivity (5G, C2x)

#### Artificial Intelligence

Learning architecture
Al based functions
Verifiable Al

## Safety und Security by Design

Fail operational
Post quantum cryptography

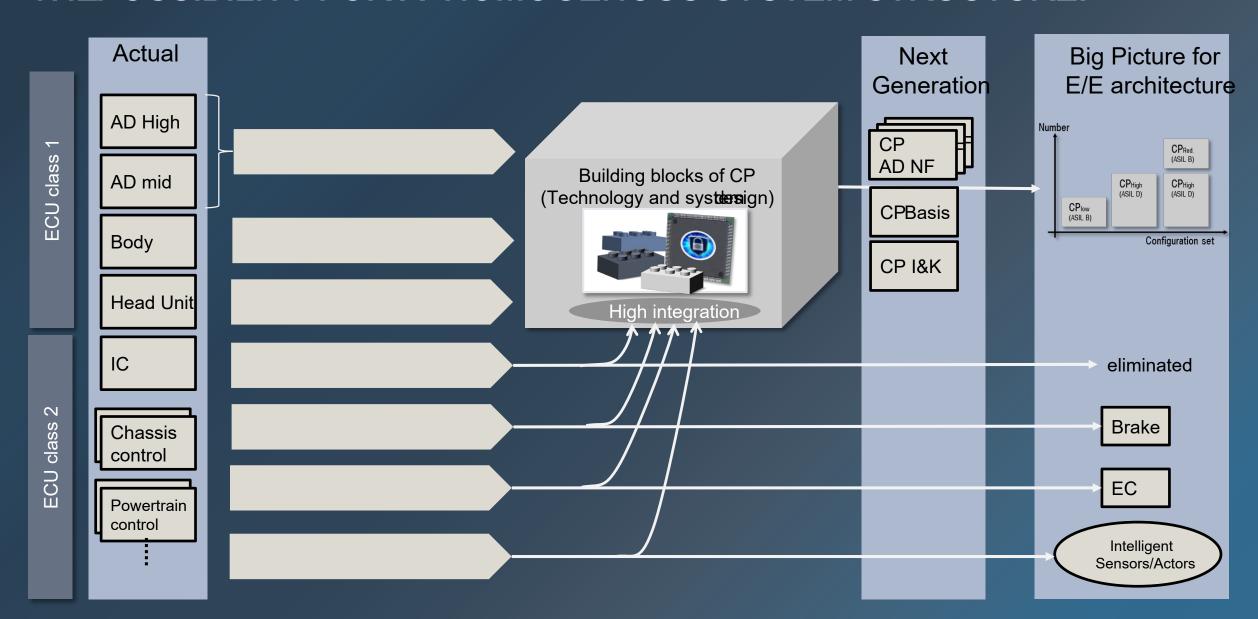
### Communication architecture

IP based communication 10MBit/s Ethernet OFDM technology

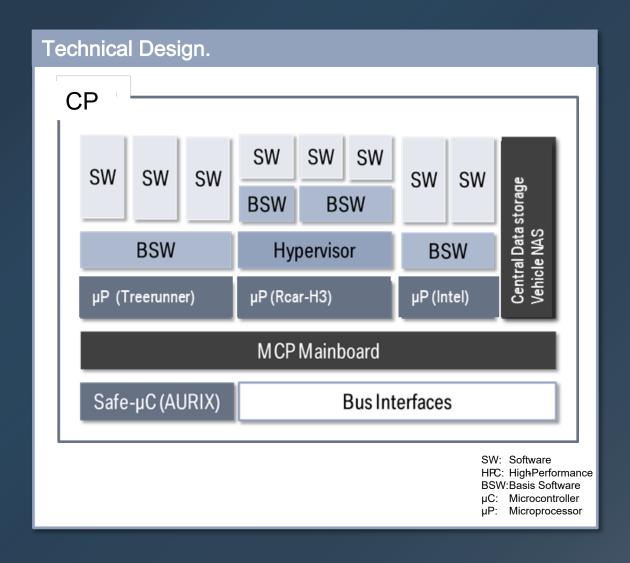
## Agile automotive Systems Engineering

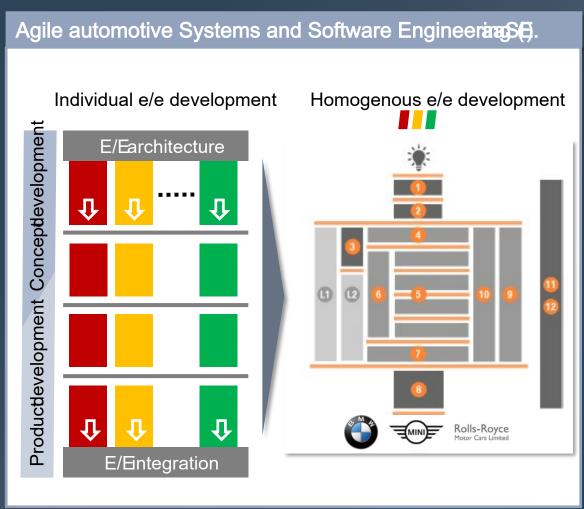
Extending modbased development Seamless agile development prod Harmonized workflows

# VISION: THEOMPUTINGLATFORMOP) CREATES SYNERGIES AND OFFERS THEPOSSIBILITY FOR A HOMOGENOUS SYSTEM STRUCTURE.

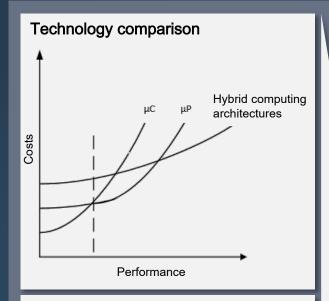


# REALIZING SUCH A COMPUTINGTFORMOP) THE TECHNICAL DESIGN AND THE DEVELOPMENT PROCESS HAS TO BE ENABLED.

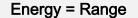




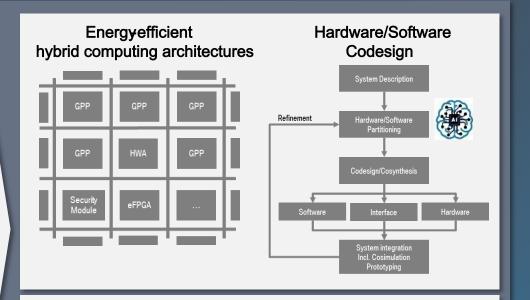
## NEW WAYS FOR DERIVING THE NEXT GENERATION PORTUGEN AND PROCESSORS FOR AUTOMOTIVE APPLICATIONS ARE NECESSARY.



- For basic functions (degake and engine control) automotive microcontrollers (µCs) are ideal.
- A direct usage of microprocessors (µPs) of the CEworld is not constructive.
- New approaches in the form of hybrid computing architectuæsenecessary.







- Hybrid computing architectures enable a-effetignt allocation of the necessary performances by a optimal interaction of general purpose processors (GPP), dedicated hardware accelerators (HWA) and embedded field programmable gate arrays (eFPGA).
- A optimal composition on system level could be derived by using hardware/software codesign.
- Over the European Processor Initiative (EPI) the first steps on the way are started. For a sustainable ability them are furt
  actions necessary.
- Enabling the automotive industry using hardware/software codesign is one of the main goals.

# EUROPEARROCESSORNITIATIVE (EPS)UPPORTS THE FURTHER ROLLOUT HIGHPERFORMANCE COMPUTING (HPC) IN AUTOMOTIVE E/E SYSTEMS.

EC Horizon2020



European 10 years research program

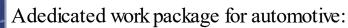
EUFPAcall (ICT-42)

#### EPI European Processor Initiative

- European approach for HPC technology for exascale super computer including a scalable HPC General Purpose Processor (GPP) with special security and reconfiguration options and dedicated hardware acceleration.
- Project budget: 120 M€, 4 years, start: 12/18



Automotive stream in EPI



- eHPC: MCU and GPP and Accelerators for automotive
- Total budget: 20M€

















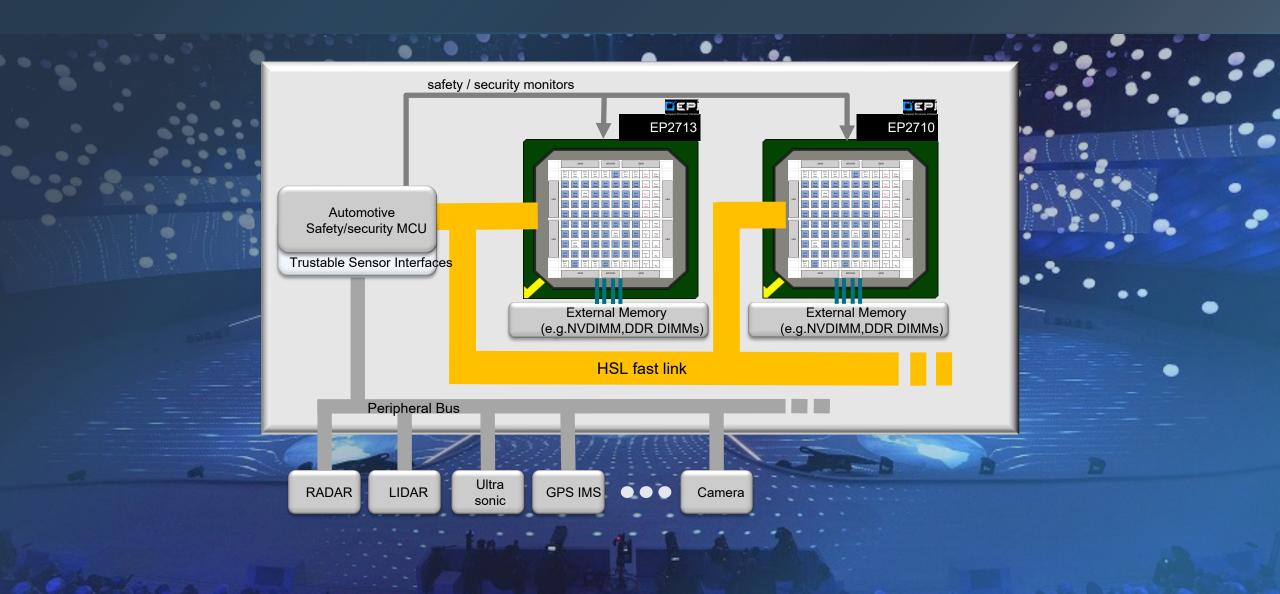






eHPC: Embedded High Performance Computing MCU: Micro Controller Unit

# A FIRSTREFERENCE IMPLEMENTATION OF AUTOMOTIVE WILL BE REALIZED OVER THE EPI PROJECT IN 2021.



WE HAVE TO HANDLE A LOT OF NEW TOPICS BUT TWO

OF THEM ARE ESSENTIALLY. **Augmented Reality** Conversational Commerce

**Digital Services** 

**Stream Analytics** 

Semantics

CoAP M<sub>2</sub>M E/E

2025 +

**Functionality** 

Intelligent Personal Assistan

Affective Computing

Αl

Machine Learning

Neuromorphic Hardwar

Sensors **Intelligent Energy** 

Infrastructure

Fail Operational

Computing **Platforms** 

Technical view:

Usingmore and moreTstandards and establishing homogenous and energy-efficient system structure.

Communication

Edge Computing 5G

Cloud

**Mixed Criticality** 

Power Efficiency

Hardware/Software codesign

Accelerators

Virtualization Privacy

Safety Security

Methodical view: Systemicthinking withoutborders, a trustful collaborationand agile working methods.

## THANK YOU FOR YOUR ATTENTION.



"TO SEE THINGS IN THE SEED,

THAT IS GENIUS."

LAO-TSE

"WHAT IS NOW PROVED WAS ONCE ONLY IMAGINED."

WILLIAMBLAKE

"THE FUTURE BELONGS TO THOSE BOLDENOUGH TO SHAPE IT."

BMWGROUP

"TO ACCOMPLISH GREAT THINGS WE MUST NOT ONLYACT, BUT ALSO DREAM."

ANATOLE FRANCE

"ALL TRUTHS ARE EASYTO
UNDERSTAND
ONCE THEY ARE
DISCOVERED, THE
POINT IS TO
DISCOVER THEM."

GALILEO GALILEI VON GO

"KNOWING IS NOT ENOUGH; WE MUST APPLY. WILLING IS NOT ENOUGH, WE MUST DO."

JOHANN WOLFGANG VON GOETHE