

Entering A New Frontier of Al Networking Innovation Gilad Shainer | Teratec 2024



APPLICATION FRAMEWORKS



PLATFORM

ACCELERATION LIBRARIES

SYSTEM SOFTWARE

HARDWARE









Control / User Access Network (North-South)

Loosely Coupled Applications	
TCP (Low Bandwidth Flows and Utilization)	
High Jitter Tolerance	
Heterogeneous Traffic Average Multi-Pathing	000 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

The Network Defines the Data Center

Al Fabric (East-West)

Distributed **Tightly-Coupled Processing**

RoCE (High Bandwidth Flows and Utilization)

Low Jitter Tolerance (Long Tail Kills Performance)

Bursty Network Capacity Predictable Performance

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North – Sou			>
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East – West



LLM Compute and Communication Profiling





Representative profile from a large scale LLM training run Communications is bursty in nature, an average bandwidth utilization is not a good network criteria









NCCL (NVIDIA Collective Communication Library) is the SDK library for AI communications - connects the GPUs and the network for the AI network operations

The Network Defines the Data Center





NVIDIA.

Spectrum-X800 Brings High-Performance Al to Ethernet Al-optimized networking for every data center

- RoCE Adaptive Routing (local and remote information, a packet granularity) Congestion Control (telemetry probes)
- Noise Isolation (multi-jobs or a single large-scale job)
- High frequency telemetry (1000x)



Spectrum-X800

- Spectrum-X800 Switch
- 51.2T bandwidth
- 64 X 800G Ports, 128 x 400G
- Adaptive routing
- Congestion control, noise isolation

BlueField-3 SuperNIC

- 16 Arm 64-Bit Cores
- 16 Core / 256 Threads Datapath Accelerator
- DDR memory interface
- ConnectX NIC
- PCIe switch

BlueField-3 SuperNIC

Effective Network Bandwidth With and Without Adaptive Routing





Spectrum-X New Class of Ethernet for Al



• Spectrum-X performance is consistent; Traditional Ethernet shows run-to-run bandwidth variability Results in 1.4x higher LLM performance (2K GPUs)

LLM NCCL AllReduce — Spectrum-X Ethernet



Optimal Placement

Average Placement Worse-Case Placement



NVIDIA Spectrum-X Generative AI Cloud Most powerful supercomputer in Israel



2,048 GPUs

- 2,560 Blue Field-3 SuperNICs
- 80+ Spectrum-x800 Ethernet switches
- Peak AI performance of 8-Exaflops



LLM Compute and Communication Profiling





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NVIDIA SHARP Scalable Hierarchical Aggregation and Reduction Protocol Technology

- In-network data aggregation med
- Multiple simultaneous outstandi
- Barrier, reduce, all-reduce, broadd
- Sum, min, max, min-loc, max-loc,
- Integer and floating-point, 8/16/32/64 bits

chanism	Switch
ng operations	
cast and more	Switch
	Switch
, or, xor, and	GPL



AN

SHARP Aggregation Node: Switch Resident

 \bigcirc Host: Data source and Destination





- 144 ports of 800G, 5x higher switch capacity
- SHARP v4 with 14.4 TFlops of In-Network Computing, 9x higher
- Adaptive routing, congestion control



Quantum-X800

Quantum-X800 InfiniBand Switch Highest-Performance Al-Dedicated Infrastructure

Quantum-X800 switch

- 144X 800G ports,
- SHARPv4 In-Network Computing
- Adaptive routing
- congestion control, and noise isolation



ConnectX-x800 InfiniBand SuperNIC

- PCIe Gen 6, PCIe switch
- Multi-host

ConnectX-800 SuperNIC



Message Size (MiB)

© NVIDIA.



Fat-tree

Exploring Topologies – Creating Routing Algorithms



Existing routing algorithms support



Dragonfly



Torus



HyperX





NVLINK Data Reduction

Rail Optimized Topology

Quantum, Spectrum

NVLINK Data Reduction



SOFTWARE DEFINED NETWORKING



Data Center on a Chip

16 Arm 64-Bit Cores 16 Core / 256 Threads Datapath Accelerator ConnectX InfiniBand / Ethernet DDR memory interface PCIe switch

BlueField Data Processing Unit



BlueField Infrastructure **Compute Platform**



SOFTWARE DEFINED STORAGE







Octopus Performance – MPI Time 32 nodes, 100 time steps, Average total time per process in seconds

Function	Host
Application	637.28
All Communications	245.99
MPI_Allgatherv	126.38
MPI_Waitall	57.60
MPI_Alltoall	37.89
MPI_Allreduce	7.44
Other	16.68

OPU Offloaded
155.49
38.15
32.74
32.02
3.87
1.96
1.56



HPC Batch Job









